

# D6KB05 – D6KB100



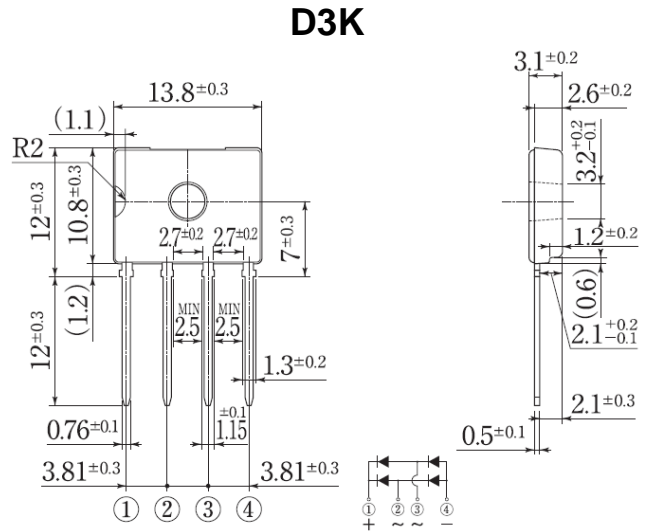
## 6.0A BRIDGE RECTIFIER

### Features

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability
- Ideal for Printed Circuit Boards

### Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: As Marked on Body
- Weight: 1.7 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- **Lead Free: For RoHS / Lead Free Version**



All Dimensions in mm

### Maximum Ratings and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

| Characteristic  | Symbol          | D6K B05                     | D6K B10 | D6K B20 | D6K B40 | D6K B60 | D6K B80 | D6K B100 | Unit             |
|---|-----------------|-----------------------------|---------|---------|---------|---------|---------|----------|------------------|
| Peak Repetitive Reverse Voltage   | $V_{RRM}$       | 50                          | 100     | 200     | 400     | 600     | 800     | 1000     | V                |
| Working Peak Reverse Voltage  | $V_{RWM}$       |                             |         |         |         |         |         |          |                  |
| DC Blocking Voltage   | $V_R$           |                             |         |         |         |         |         |          |                  |
| RMS Reverse Voltage   | $V_{R(RMS)}$    | 35                          | 70      | 140     | 280     | 420     | 560     | 700      | V                |
| Average Rectified Output Current (Note 1)   | $I_O$           | 6.0                         |         |         |         |         |         |          | A                |
|   |                 | @ $T_A = 50^\circ\text{C}$  |         |         |         |         |         |          |                  |
| Non-Repetitive Peak Forward Surge Current<br>8.3ms Single half sine-wave superimposed on<br>rated load (JEDEC Method) | $I_{FSM}$       | 125                         |         |         |         |         |         |          | A                |
| Forward Voltage (per element)   | $V_{FM}$        | 1.0                         |         |         |         |         |         |          | V                |
| Peak Reverse Current  | $I_{RM}$        | 2                           |         |         |         |         |         |          | $\mu\text{A}$    |
|   |                 | @ $T_A = 100^\circ\text{C}$ |         |         |         |         |         |          |                  |
| Typical Thermal Resistance (Note 3)   | $R_{\theta JA}$ | 35                          |         |         |         |         |         |          | K/W              |
| Operating and Storage Temperature Range   | $T_j, T_{STG}$  | -55 to +150                 |         |         |         |         |         |          | $^\circ\text{C}$ |

- Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case.  
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.  
3. Thermal resistance junction to ambient mounted on PC board with 12mm<sup>2</sup> copper pad.

# D6KB05 – D6KB100

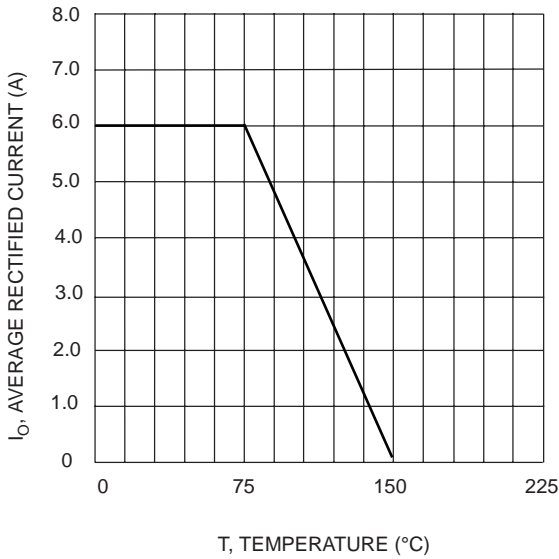


Fig. 1 Forward Current Derating Curve

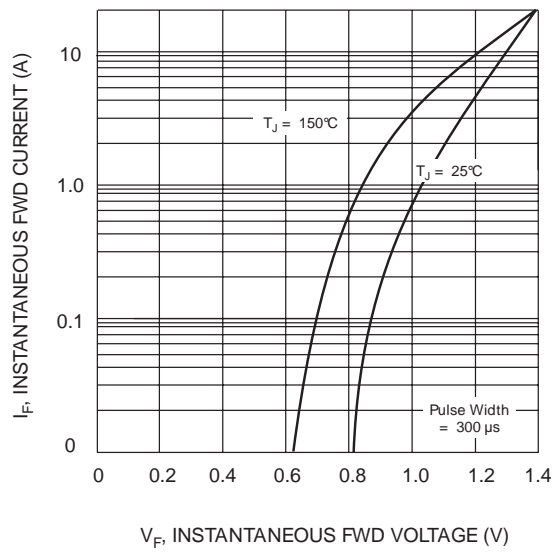


Fig. 2 Typical Fwd Characteristics

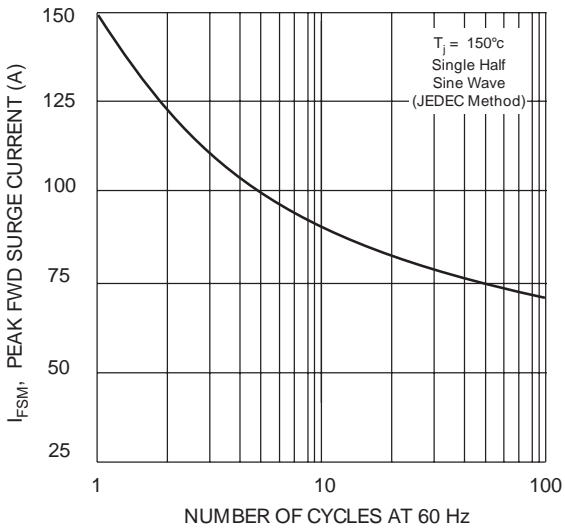


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

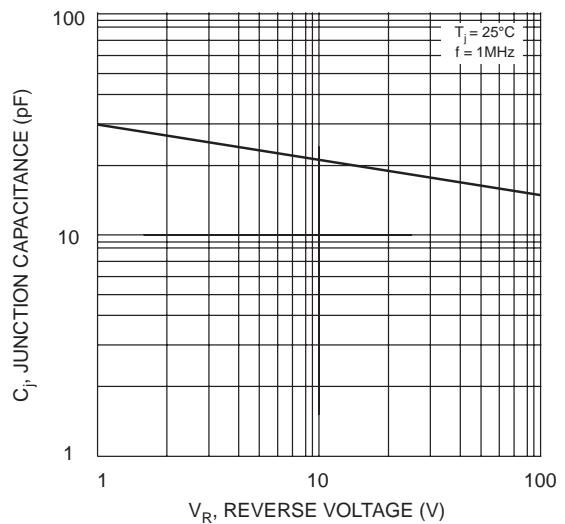


Fig. 4 Typical Junction Capacitance

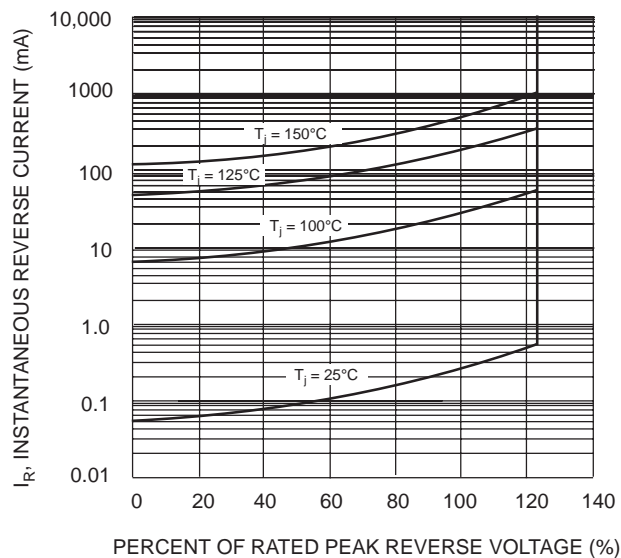


Fig. 5 Typical Reverse Characteristics